

02/09/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

PATENT DATE

SCANNED

204

44

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/779953	D F	365 438	258	2818 2813	Chen

## APPLICANTS

# Pericles

**TITLE**

Electronic memory circuit and related manufacturing method

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PTO-2040  
12/99**ISSUING CLASSIFICATION**[illegible]☐ Continued on Issue Slip Inside File Jacket

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  _____			
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